

10

FIG. 1

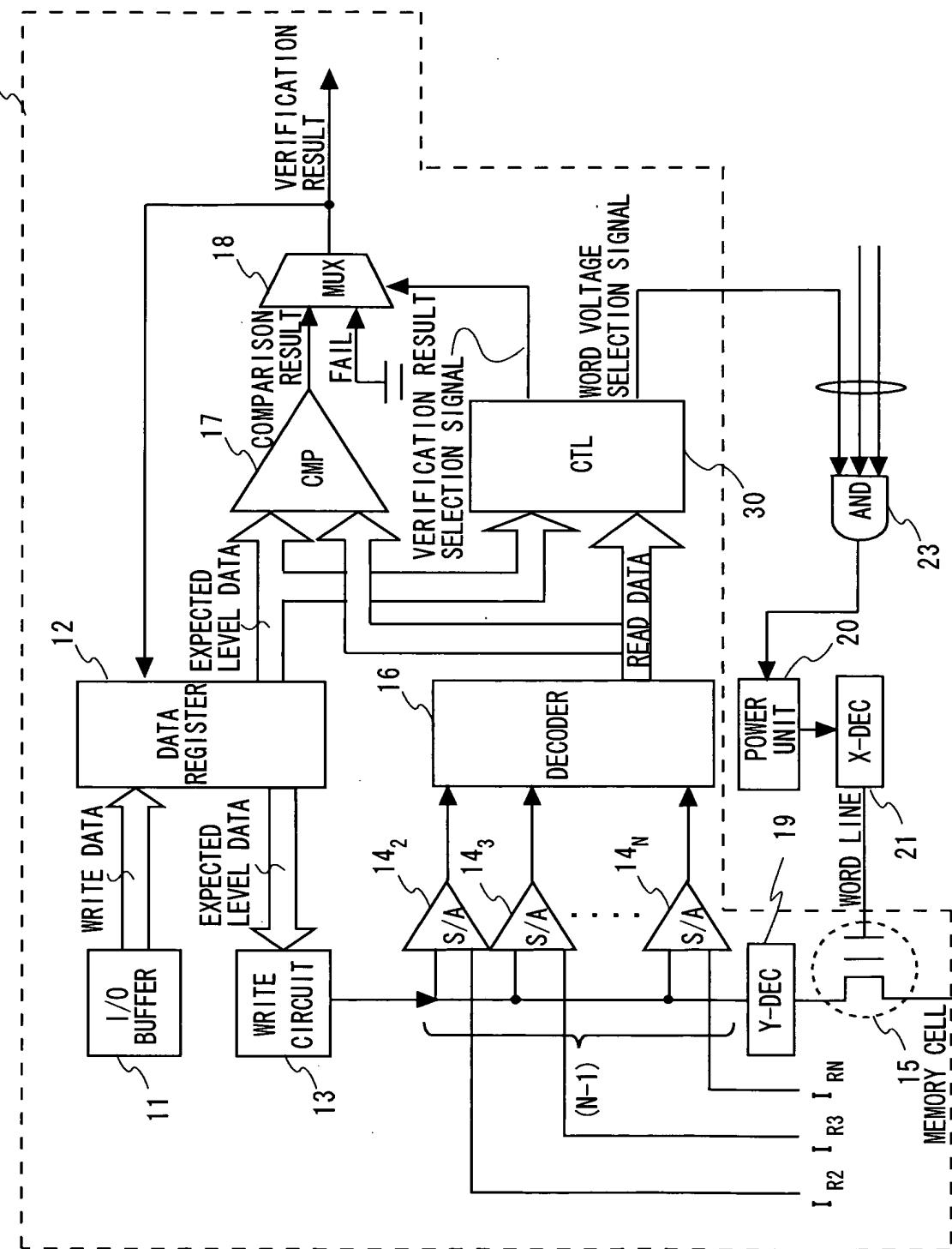


FIG. 2A

## OPERATION IN CMP

	COMPARISON RESULT	
WRITE LEVEL OF EXPECTED LEVEL DATA > WRITE LEVEL OF READ DATA	FAIL	"L"
WRITE LEVEL OF EXPECTED LEVEL DATA $\leq$ WRITE LEVEL OF READ DATA	PASS	"H"

FIG. 2B

## WORD VOLTAGE SELECTION OPERATION IN CTL

	WORD VOLTAGE SELECTION SIGNAL	
EXPECTED LEVEL DATA LVN AND READ DATA FROM MEMORY CELL NOT REACHING LVN-1, OR EXPECTED LEVEL DATA LV2 TO LVN-1	LEVEL INDICATING Vw1	"L"
EXPECTED LEVEL DATA LVN AND READ DATA FROM MEMORY CELL REACHING LVN-1, OR EXPECTED LEVEL DATA LV1	LEVEL INDICATING Vw2	"H"

FIG. 2C

## VERIFICATION RESULT SELECTION OPERATION IN CTL

	MUX SELECTION SIGNAL
CELL WITH WORD VOLTAGE Vw1 AND EXPECTED LEVEL LVN-1 OR LOWER	COMPARISON RESULT IN CMP
CELL WITH WORD VOLTAGE Vw1 AND EXPECTED LEVEL LVN	FAIL
ALL CELL WITH WORD VOLTAGE Vw2	COMPARISON RESULT IN CMP

FIG. 3

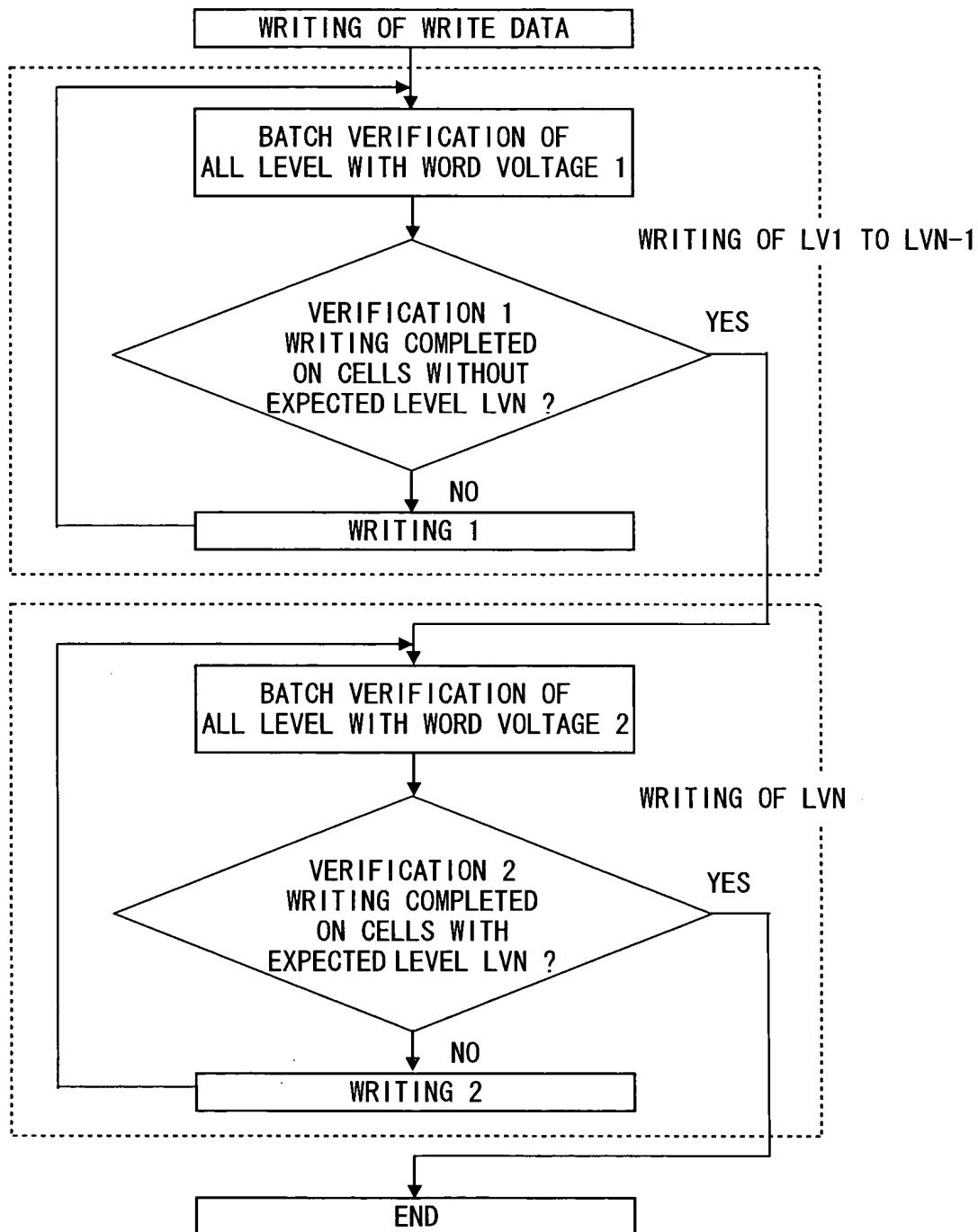


FIG. 4

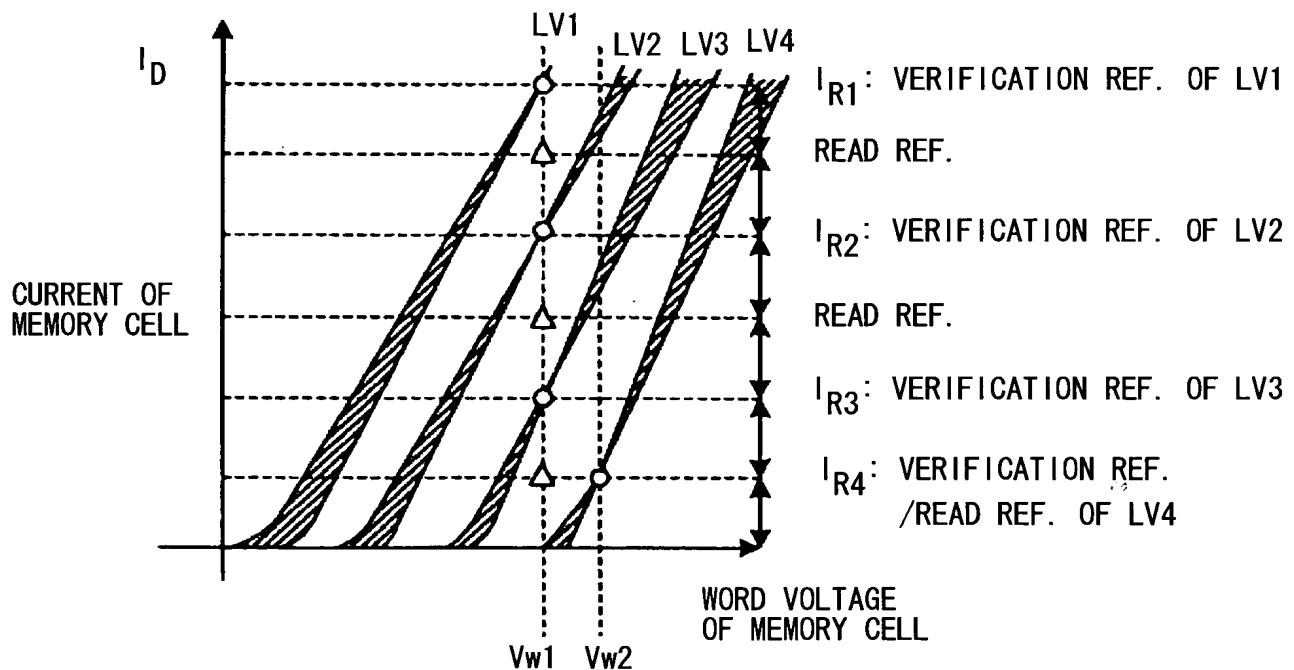


FIG. 5

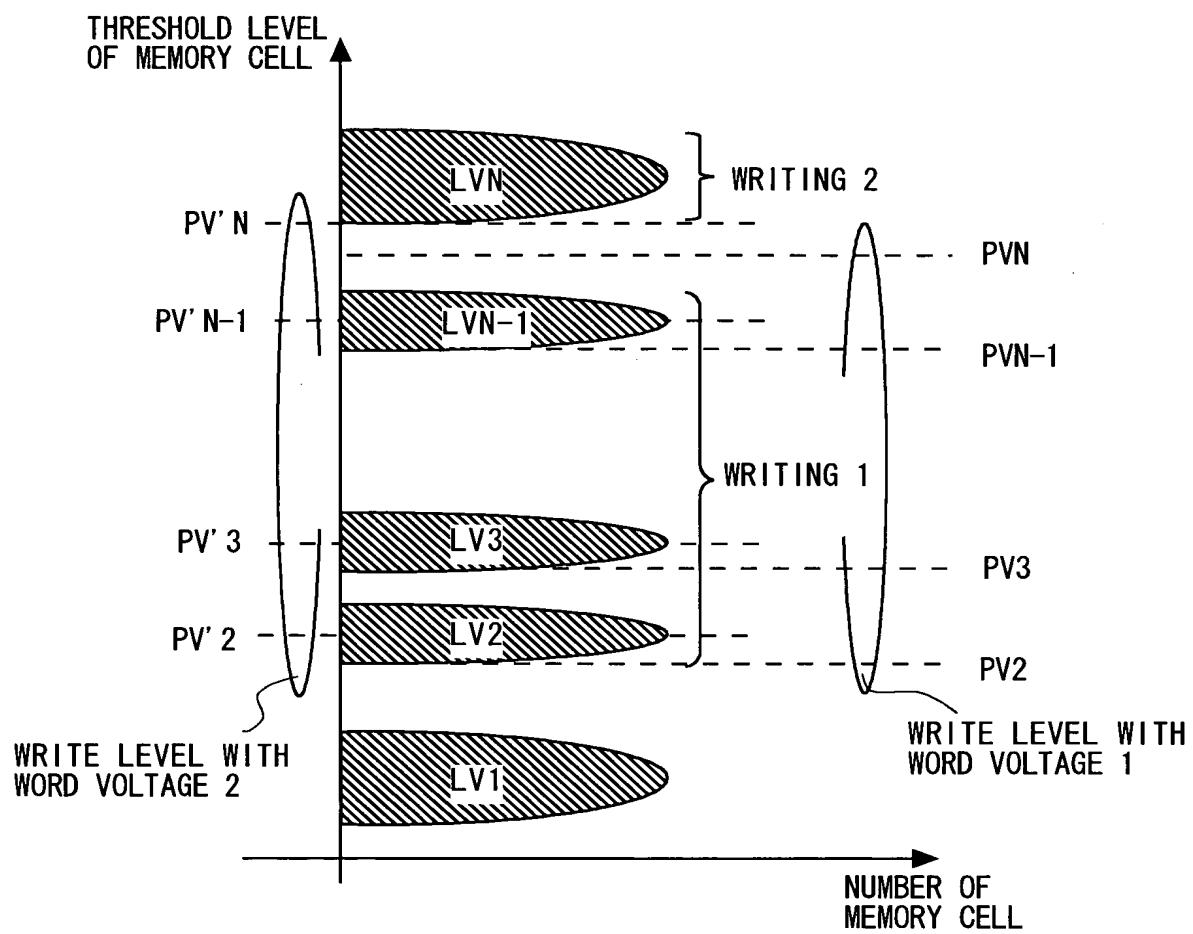


FIG. 6

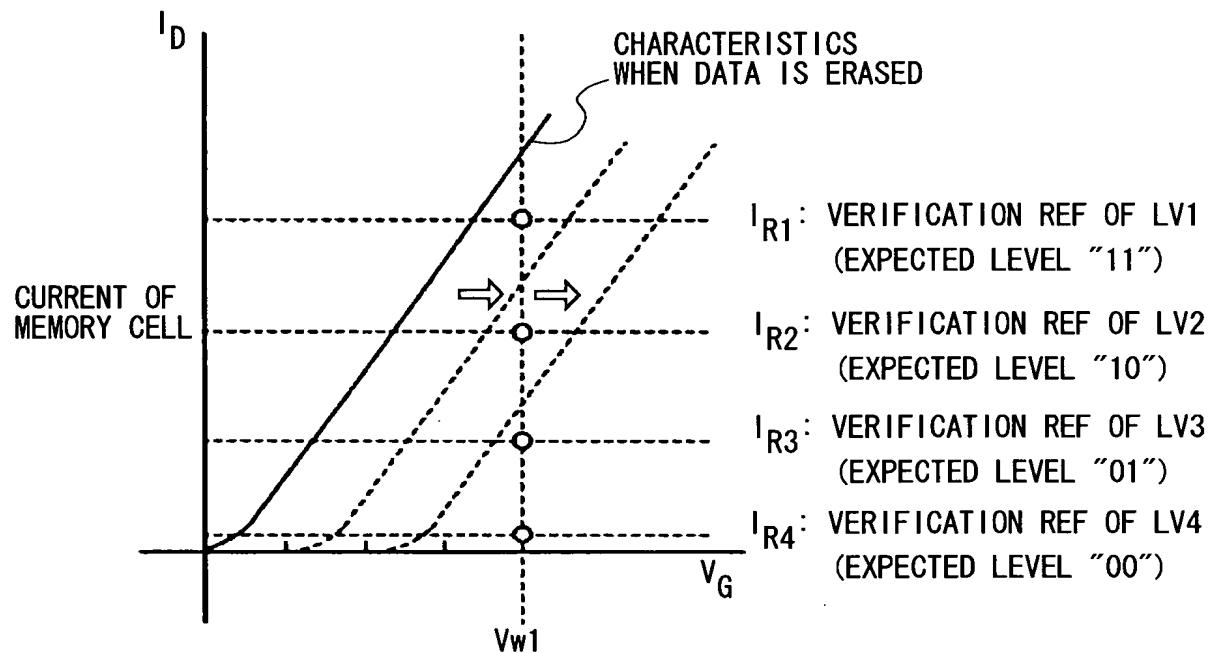


FIG. 7

## PRIOR ART

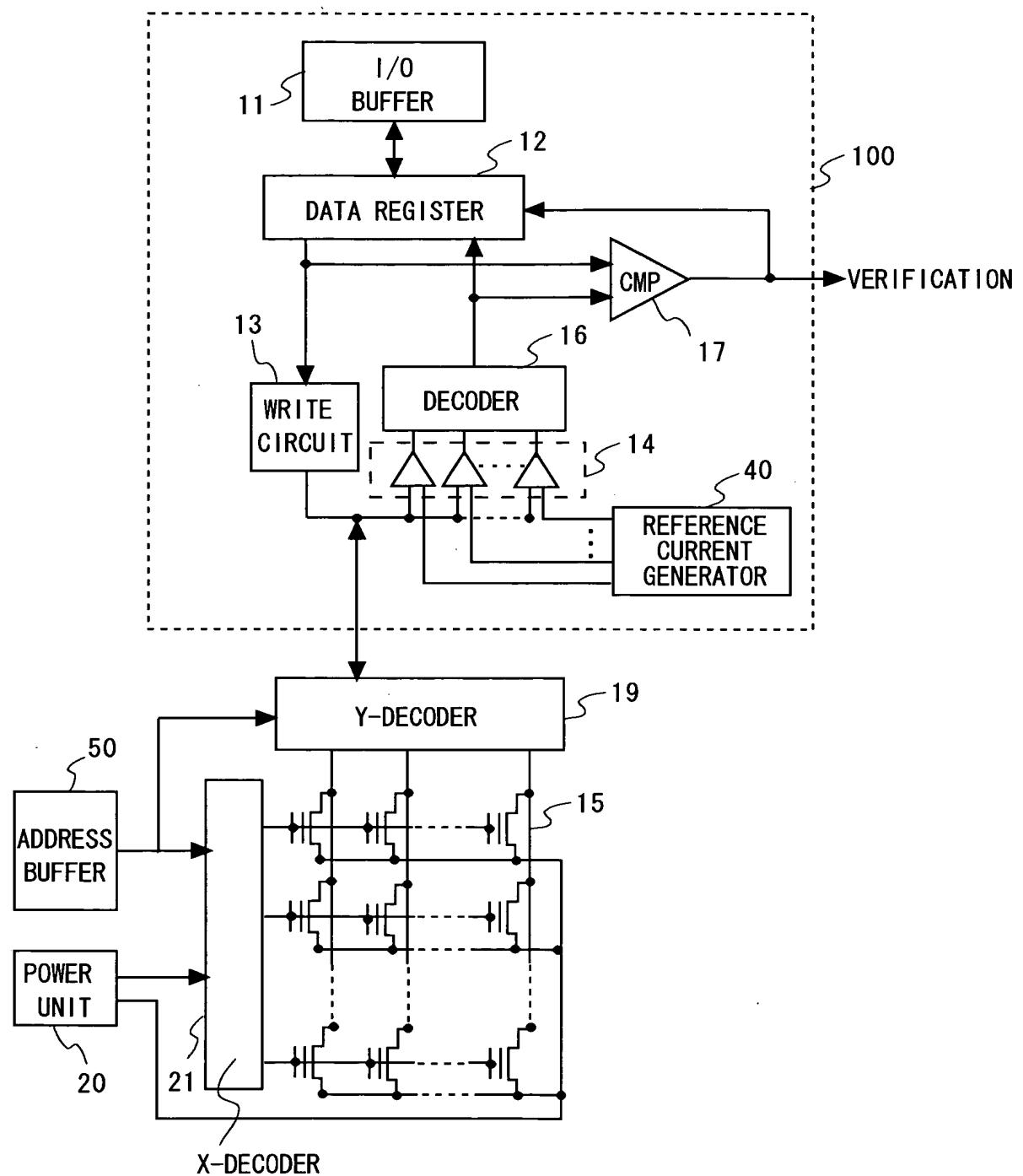


FIG. 8

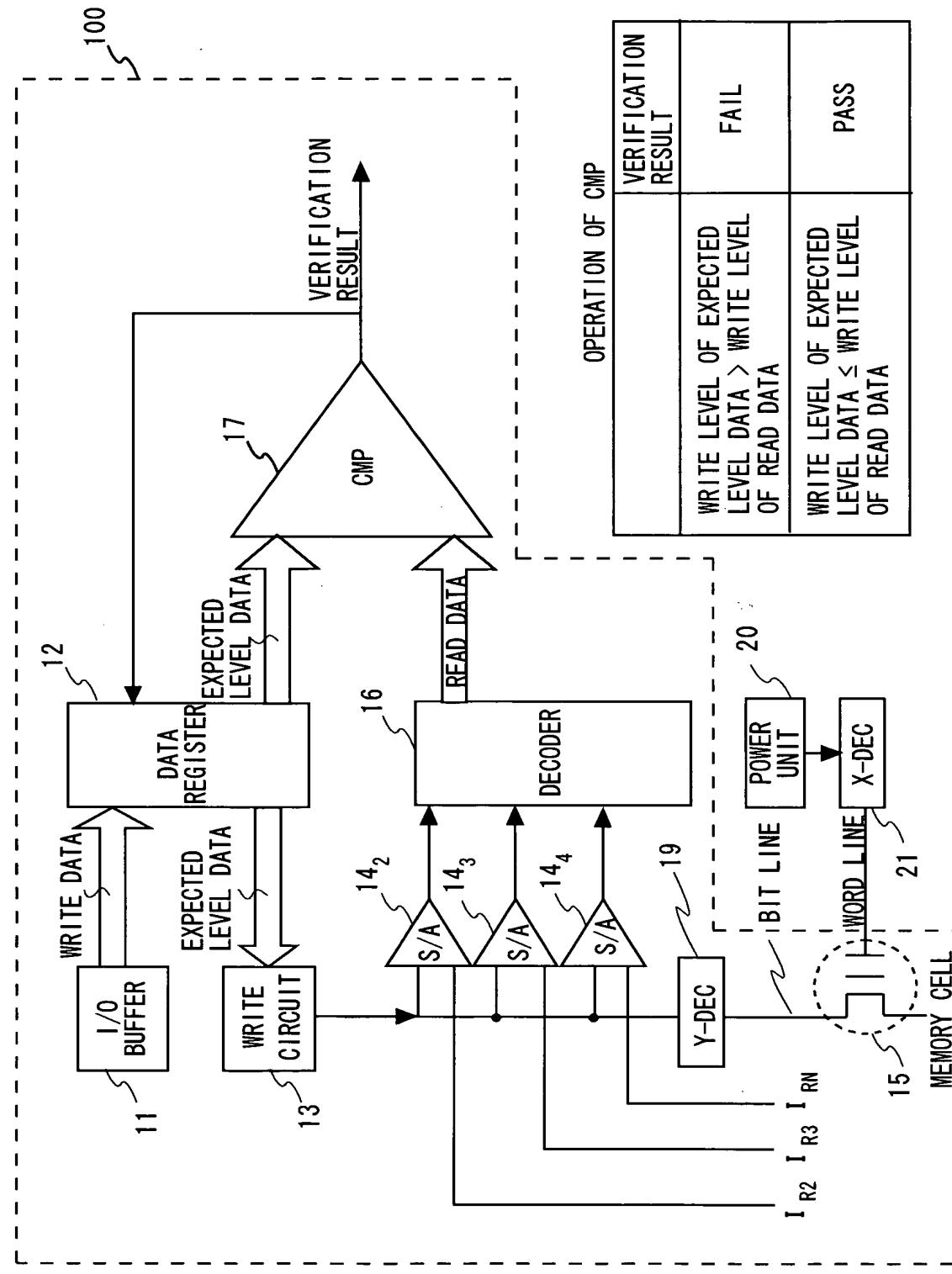


FIG. 9  
PRIOR ART

